

SANYO Semiconductors DATA SHEET

25K1740—HF Amplifiers Low-Frequency Amplifiers Analog Switches

Features

- · Adoption of FBET process.
- · Large | yfs |.
- · Small Ciss
- Small-sized package permitting 2SK1740-applied sets to be made small and slim.

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSX		40	V
Gate-to-Drain Voltage	V _{GDS}		-40	V
Gate Current	IG		10	mA
Drain Current	ID		75	mA
Allowable Power Dissipation	PD		250	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
Falametei		Conditions	min	typ	max	UIIIL
Gate-to-Drain Breakdown Voltage	V(BR)GDS	I _G =-10μA, V _{DS} =0V	-40			V
Gate-to-Source Leakage Current	IGSS	V _{GS} =-20V, V _{DS} =0V			-1.0	nA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =100μA	-2.0	-3.0	-5.0	V
Drain Current	IDSS	V _{DS} =10V, V _{GS} =0V	40*		75*	mA
Forward Transfer Admittance	yfs 1	V _{DS} =10V, I _D =10mA, f=1kHz	10	15		mS
Forward Transfer Admittance	yfs 2	VDS=10V, VGS=0V, f=1kHz	22	30		mS
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1MHz		11		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, V _{GS} =0V, f=1MHz		2.5		pF
Noise Figure	NF	VDS=10V, Rg=1kΩ, ID=1mA, f=1kHz		1.5		dB
Static Drain-to-Source On-State Resistance	R _{DS} (on)	I _D =10mA, V _G S=0V		30		Ω

Marking: IJ

 $[\]mbox{\ensuremath{}^{*}}$: The 2SK1740 is classified by $I_{\mbox{DSS}}$ as follows : (unit : mA).

Rank	3	4	5	
IDSS	40 to 52	48 to 63	57 to 75	

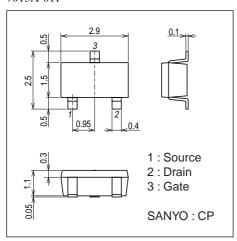
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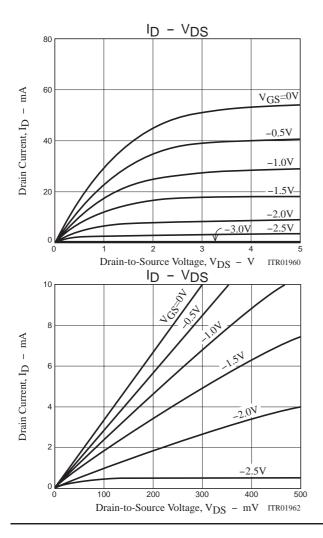
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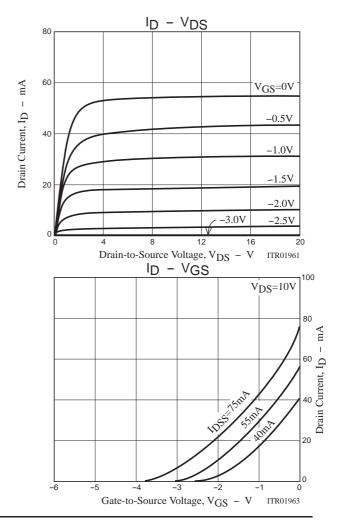
^{* :} Pulse Test Pulse Width≤2ms.

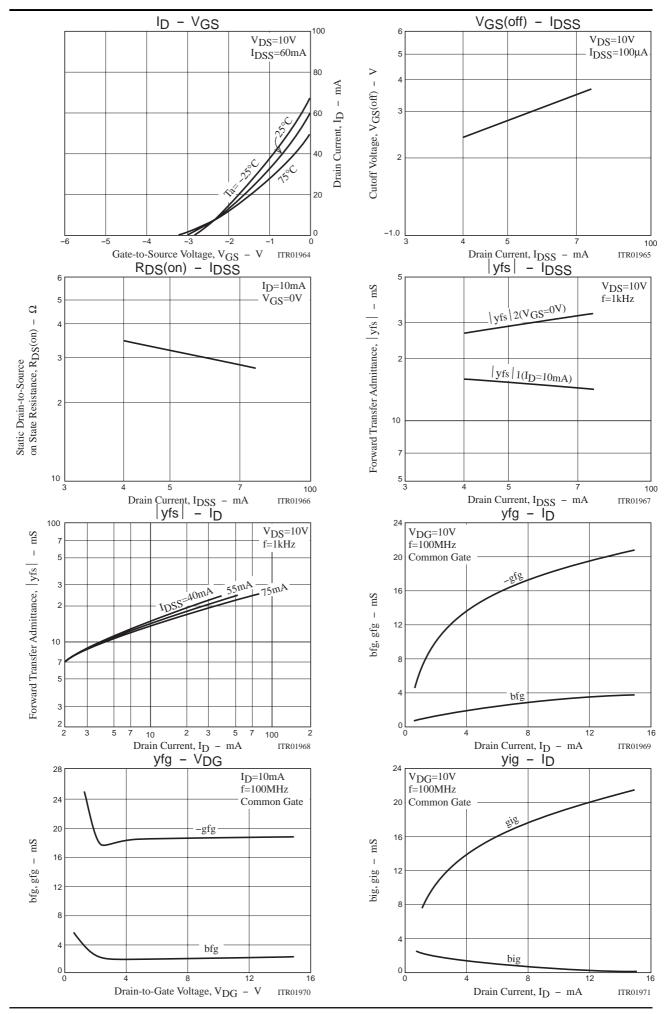
Package Dimensions

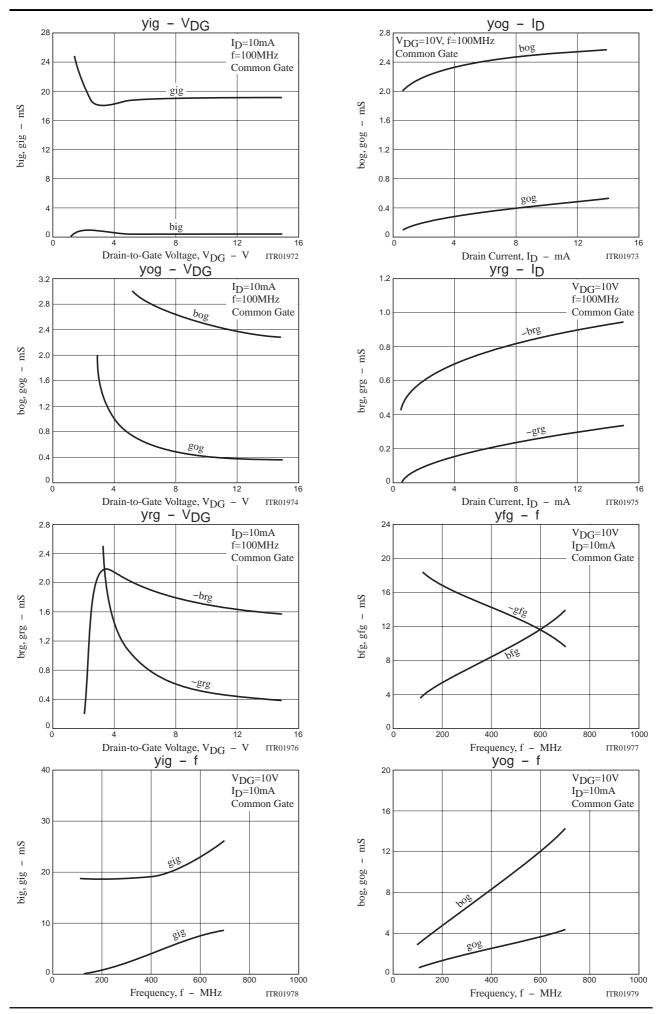
unit : mm (typ) 7013A-011

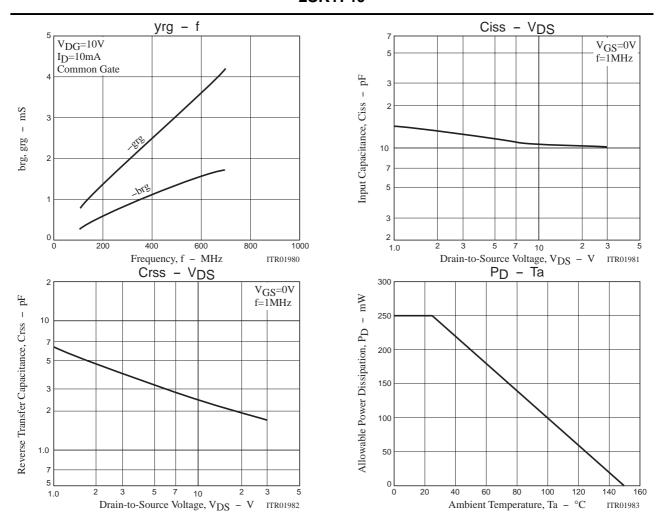












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